

WEST Search History

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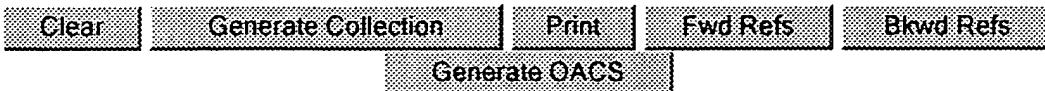
DATE: Tuesday, August 09, 2005

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<i>DB=PGPB,USPT,JPAB; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L38	L37 and (system same level same object)	6
<input type="checkbox"/>	L37	l34 and (behavio\$3 same object)	16
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<input type="checkbox"/>	L33	L32 and attribut\$ and (net or netlist)	17
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<input type="checkbox"/>	L26	L21 and port	33
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<input type="checkbox"/>	L24	L21 and (hdl same system same object)	4
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<input type="checkbox"/>	L16	l12 and L15	13
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<input type="checkbox"/>	L13	(hdl or (hardware same definition)) same generat\$4	2528
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<input type="checkbox"/>	L10	L9 and bit	60
<input type="checkbox"/>	L9	L7 and (code same (compil\$ or generat\$))	61

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Search Results - Record(s) 1 through 6 of 6 returned.

1. Document ID: US 20040143801 A1

Using default format because multiple data bases are involved.

L38: Entry 1 of 6

File: PGPB

Jul 22, 2004

PGPUB-DOCUMENT-NUMBER: 20040143801

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040143801 A1

TITLE: Structured algorithmic programming language approach to system design

PUBLICATION-DATE: July 22, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Waters, Simon Joshua	Newberg	OR	US	
Gutberiet, Peter Pius	Wilsonville	OR	US	
Takach, Andres Rafael	Wilsonville	OR	US	

US-CL-CURRENT: 716/3; 716/18



2. Document ID: US 20030216901 A1

L38: Entry 2 of 6

File: PGPB

Nov 20, 2003

PGPUB-DOCUMENT-NUMBER: 20030216901

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030216901 A1

TITLE: Design apparatus and a method for generating an implementable description of a digital system

PUBLICATION-DATE: November 20, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Schaumont, Patrick	Wijgmaal		BE	
Vernalde, Serge	Heverlee		BE	
Cockx, Johan	Pellenberg		BE	

US-CL-CURRENT: 703/13[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KUMC](#) | [Drawn D](#)**3. Document ID: US 20020133788 A1**

L38: Entry 3 of 6

File: PGPB

Sep 19, 2002

PGPUB-DOCUMENT-NUMBER: 20020133788

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020133788 A1

TITLE: Structured algorithmic programming language approach to system design

PUBLICATION-DATE: September 19, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Waters, Simon Joshua	Newberg	OR	US	
Gutberlet, Peter Pius	Wilsonville	OR	US	
Takach, Andres Rafael	Wilsonville	OR	US	

US-CL-CURRENT: 716/3; 716/18[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KUMC](#) | [Drawn D](#)**4. Document ID: US 6701501 B2**

L38: Entry 4 of 6

File: USPT

Mar 2, 2004

US-PAT-NO: 6701501

DOCUMENT-IDENTIFIER: US 6701501 B2

**** See image for Certificate of Correction ****

TITLE: Structured algorithmic programming language approach to system design

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KUMC](#) | [Drawn D](#)**5. Document ID: US 6606588 B1**

L38: Entry 5 of 6

File: USPT

Aug 12, 2003

US-PAT-NO: 6606588

DOCUMENT-IDENTIFIER: US 6606588 B1

TITLE: Design apparatus and a method for generating an implementable description of a digital system

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KUMC](#) | [Drawn D](#)

6. Document ID: US 6152612 A

L38: Entry 6 of 6

File: USPT

Nov 28, 2000

US-PAT-NO: 6152612

DOCUMENT-IDENTIFIER: US 6152612 A

TITLE: System and method for system level and circuit level modeling and design
simulation using C++

Full	Title	Citation	Front	Review	Classification	Date	Reference				Claims	KMC	Drawn D
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